Project Overview

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**Development Environment**

IDE - Atollic Truestudio v9.3   
HAL generation - CubeMX v6.0.1  
MCU - STM32F042K6  
Dev Board - NUCLEO-F042K6

**Logic Summary**

Note: Flowcharts can be found in “STM32Flowchart.draw.io” generated at [www.draw.io](http://www.draw.io).

My implementation of the task involves the use an STM32F0 as the target device. The PC is the master that uses a python-based GUI to transmit instructions through a USB to Serial converter.

To begin operation, the user types a number from 1 to 100 on the terminal on the PC. This value in encoded into a transmit message 10 bytes long. The number is placed in the instruction slot (byte 0). A crc16 is generated using this message and appended to the end. Final transmission of data to the target device is 12 bytes long. Transaction is complete when an acknowledge is received from the target. If a not-acknowledge is returned, transmission is attempted again.

If the user attempts to send several instructions, they are placed in a queue. An instruction is transmitted and acknowledge received from the target before the next is sent.

The target device uses a timer to track when 10 seconds have passed since an instruction went into effect and a pwm to generate the led binking.

When the target device receives an instruction message from the pc, it stores it in a receive buffer of 12 bytes. A crc16 calculation is performed on received data to check for any data loss. If succeeded, the instruction byte is placed into a serial buffer for implementation and an acknowledge is returned, else a not-acknowledge.

The serial buffer is a ring buffer with indexes that track which data has been processed, which is pending and how many are pending. Once an instruction is placed in the buffer, the device can take one of two actions:

1. If this is the first instruction or this instruction is coming more than 10 seconds since the transmission of the previous instruction, the system must set the pwm register and enable the timer and pwm.
2. If this instruction arrives less than 10 seconds since the previous instruction, the system only needs to update the pwm register and restart the timer once the previous instruction has completed.

When an instruction is going into effect, apart from setting the timer and pwm, the device also performs a check on the instruction. It checks if the instruction value is a multiple of 4 and/or 7. Depending on the result, it prepares a response message to be returned to the PC. This message can range from 1 to 10 bytes. A crc16 is calculated and appended to the end of the message. Final transmission size is 12 bytes.

If there are no more instructions waiting in the serial buffer queue, the device disabled the pwm and timers upon competition of the final instruction. No actions are taken until a new instruction is received.

**Project Breakdown**

STM32F042K6

1. Higher Application
   1. blink.c : Enables/Disables LED, generates and transmits response message
   2. multiples.c : Checks for multiple of 4 and/or 7 and builds response message
   3. definitions.h : Definitions for buffer sizes, ring buffer structure, return codes etc
2. Middleware
   1. serial\_port.c : UART transmit/receive, integrity check, DMA transfer data to buffer
   2. timer.c : Enable/disable timer and pwm peripherals, register timer callback
   3. crc.c : Perform crc16 check, generate crc16, append crc to buffer
   4. dma.c : Register DMA success/failure callbacks, adjust buffer indexes

PC



**Critical Design Considerations**

1. **Hardware based CRC16:**   
   Several MCU’s come with hardware CRC peripherals which are much faster. The STM32L4 does not but M4 does. The STM32F042K6 used here has a CRC32 peripherals, not CRC16. Hence, chose to implement a software CRC16. Code will function the same when porting to another STM with/without CRC peripherals.
2. **CRC16 Lookup Table:**  
   A CRC algorithm can be implemented faster by using a lookup table. Instead of calculating the modulo 2 division for each byte, a lookup table is used to improve processing time. However, it takes more space in memory.
3. **Automatic Baudrate Selection:**The STM32F0 and L4 have an ABR option for some UART peripherals. This would make the system more adaptable for different serial configurations. M4 does not, hence chosen not to implement.
4. **FIFO Ring Buffer:**A ring buffer was chosen to store valid instructions as it allows for greater freedom than a linear buffer. Data can be written to slots as soon as they are processed.   
   Buffer is static at 10 bytes. Chose not to implement a dynamic buffer as overtime, constantly reserving and freeing memory on the heap can corrupt it.

**Final Notes**

1. A critical flaw in my implementation is that the buffer is designed for 10 instructions after which previous pending instructions will start getting overwritten. A check along with ready/busy status codes should have been implemented.
2. All file headers contain feature lists stating detailed features and limitations of the library.
3. All file headers are doxygen compliant.